



## Temperature-Induced Variations in Silicon Solar Cell Efficiency and Electrical Characteristics

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DOI : <https://doi.org/10.55248/gengpi.6.0425.14161>

### ABSTRACT

The performance of silicon solar cells largely depends upon the temperature variations, which affect significant electrical parameters such as open-circuit voltage (Voc), short-circuit current (Isc), maximum power (Pmax), fill factor (FF), efficiency ( $\eta$ ), and current density (Jsc). In this research, the effect of temperature on silicon solar cell I-V characteristics is explored by monitoring their response within a temperature range of 24°C to 50°C. Experimental results show a significant decline in Voc and efficiency with temperature rise mainly because of high intrinsic carrier concentration and recombination loss. Though Isc experiences minor fluctuations, the net outcome is lowered power and efficiency output. The study recommends the use of good thermal management practices to fight high-temperature performance loss. Moreover, new photovoltaic materials such as low-temperature coefficient perovskite and tandem solar cells provide promising paths toward improved thermal stability. The present work advises the solar cell optimization design and cooling system for maximum performance under practical operation conditions.

**Keywords:** Silicon Solar Cells, Temperature Effect, Photovoltaic Performance, Open-Circuit Voltage (Voc) and Energy Conversion Efficiency

### Introduction:

Silicon solar cells have several applications in photovoltaic devices owing to their stability, efficiency, and low cost. The electrical parameters are extremely sensitive to the environment, particularly to the temperature change. Temperature changes, induced by the prolonged exposure of solar cells to sunlight, impact crucial electrical parameters such as current, output power, voltage, and overall energy conversion efficiency [1,2].

It is necessary to find out the temperature effect on the efficiency of the solar cells so they can be designed to meet the requirement for optimizing the photovoltaic system design improvement for high ambient temperature applications [3,4]. The researchers have already determined that temperature variation impacts major performance parameters including open-circuit voltage (Voc), short-circuit current (Isc), maximum power (Pmax), fill factor (FF), efficiency ( $\eta$ ), and current density (Jsc) [5,6]. It is seen to occur on an increase in temperature due to higher intrinsic carrier concentration and high recombination rates in the silicon material. Isc may, however, increase at mid-tolerant temperatures but decrease at high temperatures due to high recombination losses [7].

As photovoltaic systems operate under different climatic conditions, studies on the effect of temperature variation on solar cell efficiency are also of very high importance. Temperature variation degradation in performance can lead to efficient cooling techniques, new materials, and system design optimization for improved thermal stability [8,9]. Low-temperature coefficient photovoltaic materials such as perovskite and tandem solar cells also hold bright prospects for reducing degradation from temperature loss [10,11].

Temperature dependence of silicon solar cell I-V curves has been investigated by examining their electrical parameters at different operating temperatures. Conclusions derived from the variations of Voc, Isc, Pmax, FF,  $\eta$ , and Jsc are findings that result in valuable outcomes of the research as thermal limits of silicon photovoltaic technology. They can be utilized for solar cell improvement and energy efficiency method design, particularly at high temperatures.

## MTHOD

For the experiment, a commercially available silicon solar cell was subjected to cyclic temperature to allow for electrical performance observation. The solar cell was integrated into an electric circuit with a voltmeter, ammeter, rheostat, and a variable light source that could emit standard solar radiation. For the study of the effect of temperature, the cell was placed under controlled heating conditions composed of a temperature-controlled heat source whose temperature setting was between 24°C to 50°C. Temperature changes were recorded and intermittent heating of the cell surface was maintained with a digital thermometer of  $\pm 0.1^\circ\text{C}$  precision.

The solar cell was subjected to a continuous illumination of  $1000\text{ W/m}^2$  intensity light, the standard test condition. Current and voltage were measured at each step in temperature at 24°C, 35°C, 45°C, and 50°C using a high-accuracy digital multimeter. These tests presented the dominating electrical parameters including short-circuit current ( $I_{sc}$ ), open-circuit voltage ( $V_{oc}$ ), maximum power point ( $P_{max}$ ), fill factor (FF), efficiency ( $\eta$ ), and current density ( $J_{sc}$ ). These readings were taken three times to ensure reproducibility and accuracy, and respective averages were taken for measurement.

All measurements taken were compared by graphing I-V curves for different temperatures to search for trends and deviations in performance. Efficiency and power delivered were calculated using simple photovoltaic equations, comparing the outcome with theory as well as existing work. The paper highlights a review of the thermal performance of silicon solar cells and what effective cooling systems would need to provide improved efficiency at high levels of temperature.

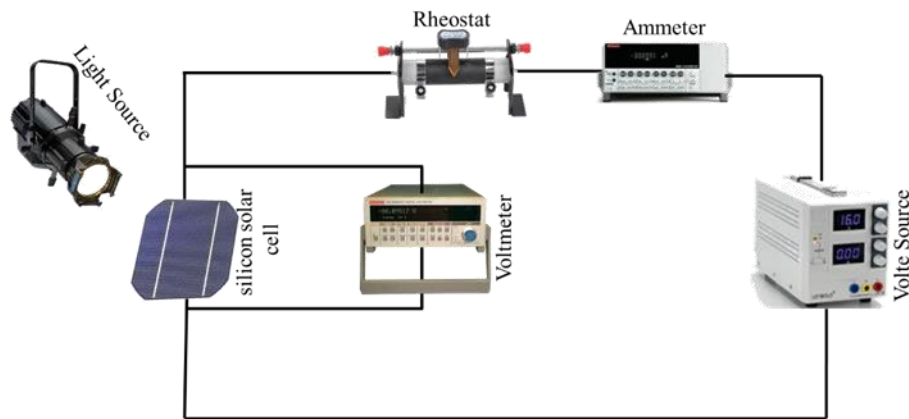
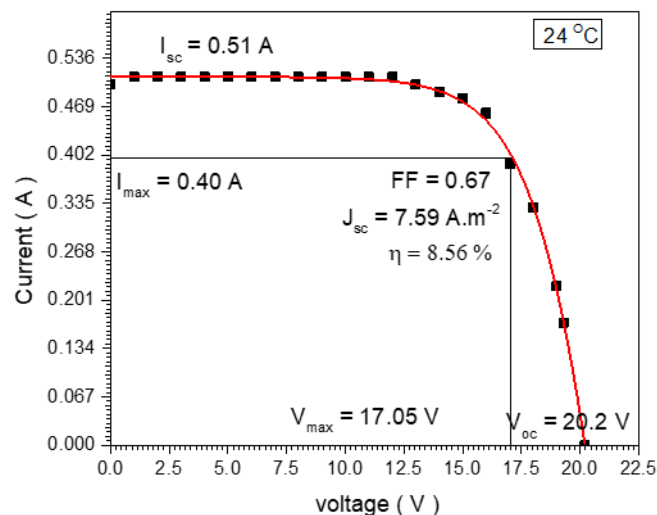
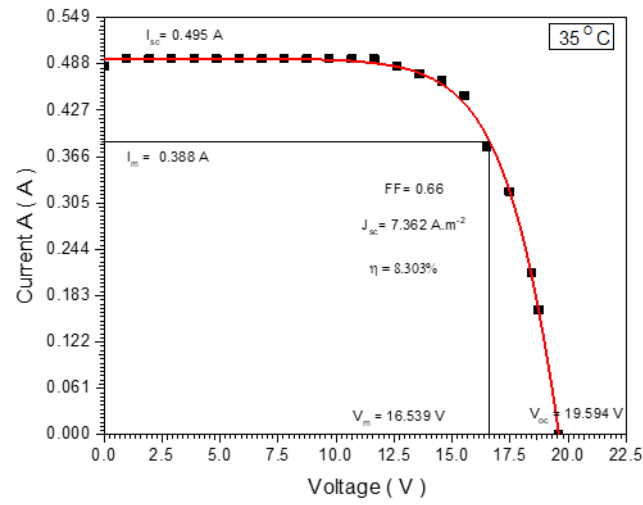
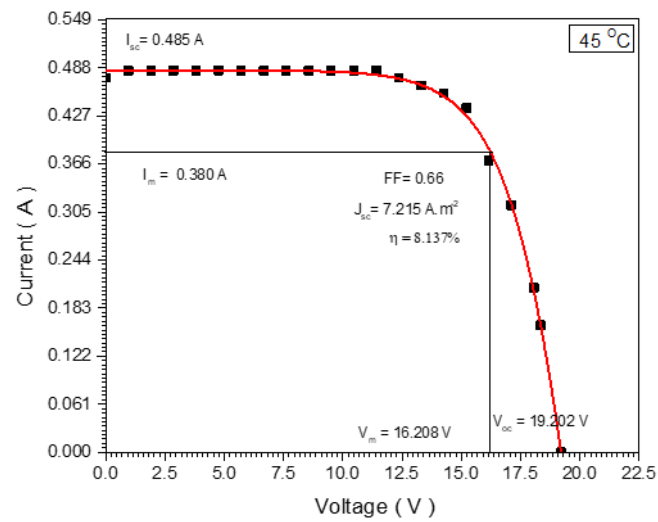


Fig. 1- Schematic diagram of IV circuit of thin film detector.

## RESULT & DISCUSSION

Experimental results reveal a high interconnectedness between the temperatures and silicon solar cell performance. The most critical electrical parameters like the open-circuit voltage ( $V_{oc}$ ), short-circuit current ( $I_{sc}$ ), current density ( $J_{sc}$ ), maximum power ( $P_{max}$ ), fill factor (FF), and efficiency ( $\eta$ ) experience outstanding fluctuations at different temperatures. These fluctuations are proofs of silicon photovoltaic temperature limitations.



**Fig. 2 - I-V curves of silicon solar cell at 24°C temperature degree****Fig. 3 - I-V curves of silicon solar cell at 35°C temperature degree****Fig. 4 - I-V curves of silicon solar cell at 45°C temperature degree**

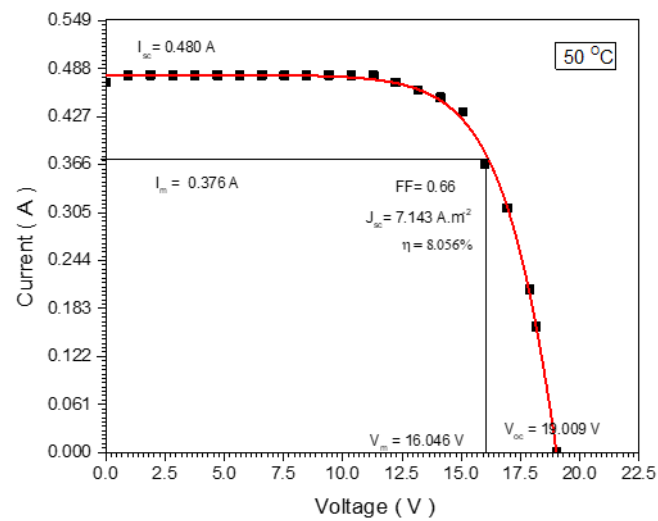


Fig. 5 - I-V curves of silicon solar cell at 50°C temperature degree

Table 1 Cell Parameters effect by different temperature degrees (24,35,45 and 50) OC

	Temperature Degree (°C)			
	24	35	45	50
Short Circuit Current Isc (A)	0.500	0.495	0.485	0.480
Maximal Current Im (A)	0.400	0.388	0.380	0.376
Maximal Voltage t Vm (V)	17.05	16.539	16.202	16.046
Open Voltage Voc (V)	20.200	19.594	19.202	19.009
Fill Factor FF	0.67	0.66	0.66	0.66
Density Current Jsc (mA.cm-2)	7.590	7.362	7.215	7.143
Efficiency η %	8.560	8.303	8.137	8.056

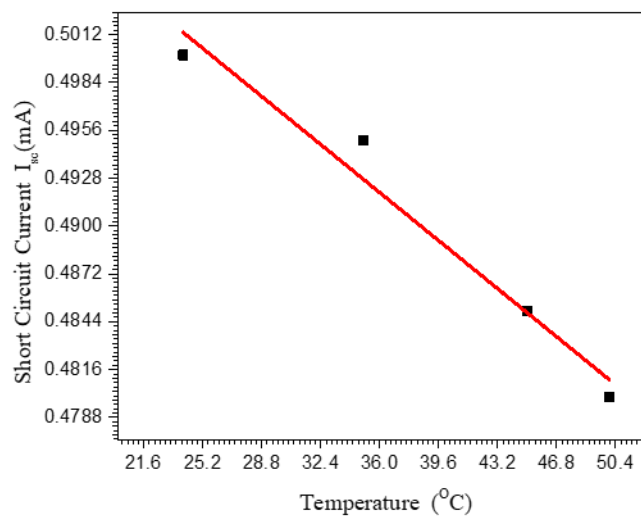


Fig. 6 - the relationship between temperature and short circuit current of silicon solar cell

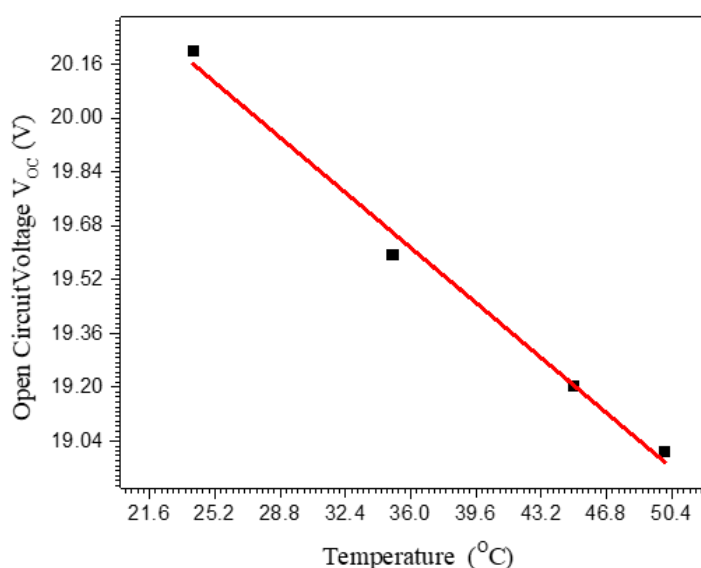


Fig. 7 - the relationship between temperature and versus open circuit voltage of silicon solar cell

## DISCUSSION

The results indicate that  $V_{oc}$  decreases linearly as temperature increases. The decrease is a result of the increased intrinsic carrier concentration; therefore, the charge carrier recombination rises, and the p-n junction built-in potential lowers [1,3]. The results confirm that  $V_{oc}$  decreases from 20.2V at 24°C to 19.009V at 50°C, as agreed with the negative temperature coefficient of silicon solar cells. Since voltage determines the level of power being delivered, this reduction is also responsible for playing a large part in reducing the overall efficiency [13].

$I_{sc}$  is more dynamic in trend with the initial increase with moderate temperatures followed by the gradual fall with rising temperatures. This is to be attributed on the basis of massive generation of charge carriers due to thermal activation for small rises in temperature that is eventually dominated by rising recombination losses with rising temperatures. Although the  $I_{sc}$  difference is of lesser magnitude than that of  $V_{oc}$ , it also shares in the net loss of efficiency [14,15].

In contrast,  $P_{max}$  reduces only when temperature increases. Since the maximum power output is a function of voltage and current, the reductions in  $V_{oc}$  and  $I_{sc}$  have a proportionate effect on  $P_{max}$ . Methods of thermal management thus need to balance against power loss during high temperatures. The fill factor (FF), however, does not change much within the range of temperature, so resistive loss and inner impedance are not affected much by changing temperatures [16].

Efficiency ( $\eta$ ) also decreases with increasing temperature, mainly due to the combined contribution of  $V_{oc}$  and decrease in  $P_{max}$ . The outcome reveals a decrease in efficiency from 8.56% at 24°C to 8.06% at 50°C, confirming the need for proper cooling systems in real applications. Also,  $J_{sc}$  decreases consistently from 7.59 mA/cm<sup>2</sup> to 7.143 mA/cm<sup>2</sup> with increasing temperature, further proving the adverse influence of excess heat on charge carrier kinetics [17,18].

These findings align with the literature, wherein silicon solar cell operation is adversely affected by temperature. Comparison to contemporary literature indicates that the existing understanding of the known temperature sensitivity of silicon photovoltaics places additional emphasis on developing new materials exhibiting improved thermal stability, such as perovskite and tandem solar cells. Cooling structure integration, material content optimization, and new photovoltaic architectures can minimize the negative effect of temperature fluctuation on the solar energy conversion process [19,20].

## CONCLUSION

This study confirms temperature as a leading parameter in the electrical performance of silicon solar cells. Reduction in open-circuit voltage and overall efficiency with increasing temperature are proofs of the inherent thermal sensitivity of silicon photovoltaics. Short-circuit current didn't undergo harsh fluctuation even though, whereas the overall trend indicates reduced performance with increased temperatures. Reduction in the maximum power generated and efficiency validates thermal control mechanisms in the practical usage of solar cells.

To overcome the adverse impact of thermal fluctuation, future research has to improve coalescence of cold systems, cell design optimization, and novel photovoltaic material having improved thermal stability. Past studies on thermally stable technologies such as perovskite and tandem solar cells are maybe a treasure trove of goldmines from which the solution is waiting to make solar energy devices work at an elevated temperature.

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