



Power Flow Improvement by Designing and Simulating a Unified Power Flow Controller with Solar Power: A Case Study

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ABSTRACT

A flexible AC transmission system (FACTS) device called Unified Power Flow Controller (UPFC) can adjust the voltage, phase angle and impedance of a transmission line to regulate the flow of real and reactive power. This paper shows how to design and simulate UPFC using amplifiers and rectifiers in MATLAB/SIMULINK. It also evaluates the impact of UPFC on the power quality and stability of a transmission line using data from GNDEC, Ludhiana. The results indicate that UPFC can improve the power flow capacity and ensure power continuity in the power system. The paper also contrasts the results with a reference system without UPFC to prove its effectiveness.

Keywords: Unified power flow controller (UPFC), Flexible AC transmission system (FACTS), Power flow capability, Power quality, Voltage control, MATLAB, SIMULINK, Design and Implementation, Reactive power control.

1. Introduction

One of the challenges in power system operation and planning is to ensure the efficient and reliable transmission of power from the generation sources to the load centres. The transmission lines are often subjected to various constraints such as thermal limits, voltage limits, stability limits and contingency conditions. These constraints can affect the power flow capability and the power quality of the transmission lines. Therefore, there is a need for devices that can control the power flow and the power quality of the transmission lines. A unified power flow controller (UPFC) is one such device that can manipulate the voltage, phase angle and impedance of a transmission line to regulate the real and reactive power flow. It is a type of flexible AC transmission system (FACTS) device that has the advantage of independent control of reactive compensation on the shunt side of the line. This can help to improve the voltage profile and reduce the losses in the transmission line. This paper presents the design and implementation of UPFC using amplifiers and rectifiers in MATLAB/SIMULINK software. It also evaluates the performance of UPFC on the power quality and stability of a transmission line using data from GNDEC, Ludhiana. The data is inserted into the SIMULINK model as input and the results are obtained in the form of output. The results show that UPFC can enhance the power flow capability and maintain the continuity of power in the power system.

Nomenclature

- A. AC of Alternating Current and DC for Direct Current
- B. FACTS of Flexible AC Transmission System
- C. SSSC of Static Series Synchronous Compensator
- D. STATCOM of Static Synchronous Compensator
- E. UPFC of Unified Power Flow Controller
- F. UPQC of Unified Power Quality Conditioner
- G. VS of Voltage Source
- H. VSC of Voltage Source Converter

1.1 Literature Review

FACTS devices have received more attention and publications in the last five years. Many studies have focused on UPFC, one of the devices that can enhance power flow and stability in transmission and distribution systems. A review of some relevant papers on UPFC is presented in the following subsection. The results show that UPFC can improve voltage stability and reduce reactive losses in the power system.

Table 1. Literature Review for UPFC.

Serial No.	Authors Name	Year of publish	Technique	Use and working	Testing Software
1.	Akshay and Shelly [5]	2019	FACTS devices control power	Reducing losses, and improving stability	PSAT/MATLAB simulates FACTS
2.	Ayman Alhejji <i>et al</i> [14]	2020	C-UPFC with AGOA for OPF	Power flow and voltage control	MATLAB/SIMULINK simulation
3.	Bhuvnesh Rathore <i>et al</i> [16]	2021	WAN with UPFC for protection	Fault analysis and power control	MATLAB/SIMULINK simulation
4.	Debidasi and Sidhartha [4]	2021	UC-UPFC with FOPIDF for LFC	Frequency regulation and control	MATLAB/SIMULINK simulation
5.	Jamal Alnasseir [2]	2022	FACTS with PV curves for stability	Voltage control and optimal placement	MATLAB/SIMULINK simulation
6.	Kumarasabapthy and Manohran [6]	2015	UPFC with fuzzy logic controller	Voltage sag and harmonic reduction	MATLAB/SIMULINK simulation
7.	Parvathy and Sindhu [13]	2015	Decoupled control with UPFC	Power flow and voltage analysis	MATLAB/SIMULINK simulation
8.	Prabhat and Surya [22].	2014	UPFC and SSSC with decoupled control	Power flow and voltage stability	MATLAB/SIMULINK simulation
9.	Pavlos and Nikos [14]	2019	UPFC with intelligent control	Power system stability improvement	MATLAB/SIMULINK simulation
10.	Ravindra and Vaibhav [15]	2019	UPFC with active and reactive coordination control	Power flow and voltage adjustment	MATLAB/SIMULINK simulation
11.	S. Birick [18]	2018	UPFC with STF control	Voltage sag and distortion reduction	MATLAB/SIMULINK simulation
12.	Saeed Rezaeian Marjani <i>et al</i> [20]	2022	UPFC with probabilistic-based technique	Power system reliability enhancement	MATLAB/SIMULINK simulation
13.	Setu and Maulik [3]	2021	UPFC with computer simulation software	Voltage stability improvement in renewable energy sources.	MATLAB/SIMULINK simulation
14.	Shradha and Chandrakala [19]	2019	UPFC with VSM model	Power flow improvement and stability analysis	MATLAB/SIMULINK simulation
15.	Yousef Islam Djilani Kobibi <i>et al</i> [25]	2022	FACTS with CPF technique	Voltage stability evaluation	MATLAB/SIMULINK simulation
16.	Alsammak <i>et al</i> [34]	2021	UPFC with 48-pulse GTO multilevel inverters	Harmonic reduction and power flow control	MATLAB/SIMULINK simulation
17.	Bhwoorjar and Jagtap [32]	2022	UPFC in grid-connected hybrid solar photovoltaic power system	Power flow and voltage control	MATLAB/SIMULINK simulation
18.	Biswas and Nayak [26]	2021	UPFC-compensated transmission lines connecting offshore wind farms	Fault detection and classification	EMTDC/PSCAD and D-SPACE DS 1103 simulation
19.	Devassy and Singh [27]	2021	PV-B-UPQC with automatic switchover	Power quality improvement and clean energy generation	MATLAB/SIMULINK simulation

1.2 Research Gap

The previous subsection reviewed some papers that studied the power flow enhancement of a three-phase line and unified power flow controller (UPFC) and the SIMULINK model of UPFC. However, none of these papers considered the integration of solar panels (PV ARRAY) with the UPFC SIMULINK

model. This is a potential research gap that needs to be addressed, as solar panels can provide an additional source of power and reduce the dependency on conventional generation. Therefore, this study aims to investigate the impact of solar panels on the power flow and stability of the system with UPFC.

1.3 Problem Formulation

To clear the voltage, sink as well as surge, UPFC is used. Amplifiers and rectifiers were used in the design and engineering of Unified Control. Changes that are actual and reactive inappropriate control orientations on the receiver side. The quality of the energy used in Unified Power Flow Control is tested using SIMULINK from MATLAB. Generally, UPFC is used to enhance the power flow capability of the transmission lines. UPFC is an abbreviation for a unified power flow controller. On the other hand, UPFC is a device and a FACTS family member. It can help in the control of basic parameters like voltage, phase angle and impedance to control real and reactive power flow of transmission lines. It is also much used due to its independent control ability of reactive compensation shunt side of the line. Therefore, in its overall operational output, UPFC becomes the controller for controlling a transmission line's reactive, active power and voltage. Power system stability and power quality issue both topics are contained the same problem for reduction of voltage, sink as well as surge and therefore FACTS devices introduce several devices to solve these errors. From the FACTS family, a UPFC device came its unique advantages which use in the power system for total reduction of these errors. After the introductory part of UPFC, there is one more introductory chapter which discusses SOLAR POWER which can be used for running the UPFC in the SIMULINK environment of MATLAB software. The UPFC model was designed by many researchers and due to many numbers of paper readings then a conclusion came in front that is a case study on circuit system design and modelling of the SOLAR POWER and UPFC in the SIMULINK tool of MATLAB software.

1.4 Methodology

This paper will use MATLAB software and SIMULINK to design a UPFC model for voltage stability in a power system with a grid connection and solar power. The UPFC model will be applied to a transmission line to examine the voltage stability and power flow capacity of the power system. The paper will use the solar panel data and parameters from GNDEC (Guru Nanak Dev Engineering College) College, Ludhiana, Punjab, India to generate SOLAR AC POWER. The paper will also use the UPFC data and parameters from the reference paper.

1.5 Present Work

1.5.1 Introduction

The UPFC is a FACTS device that combines both STATCOM and SSSC. The design of both SSSC and STATCOM has been studied in several papers. However, to understand how it can be precisely designed in the SIMULINK environment, the MATHWORK website was consulted. Therefore, reference number 59 provides a SIMULINK file of UPFC with three phase sources having five errors as shown in Figure 1.

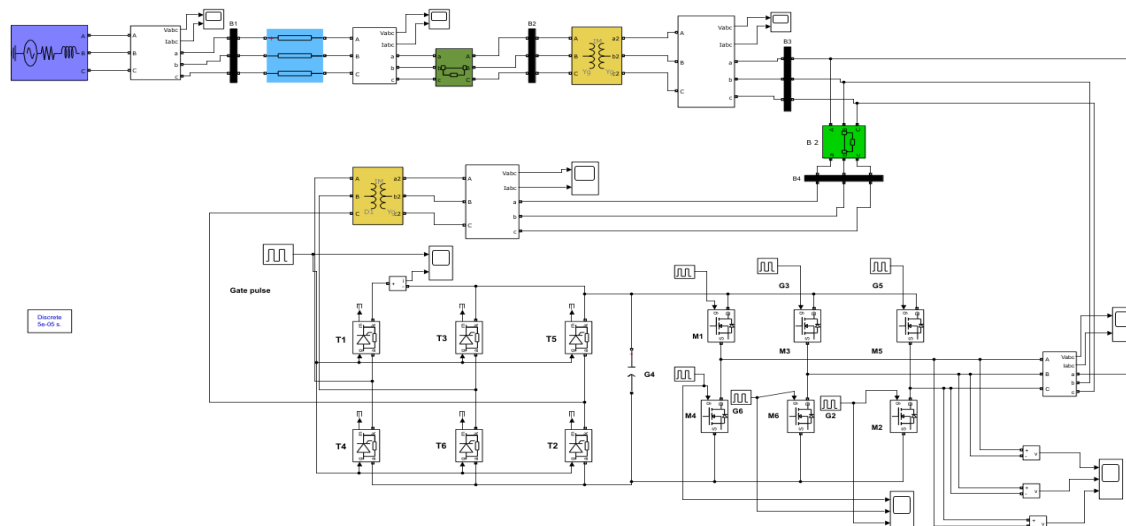


Figure 1. Original UPFC SIMULINK Model with THREE-PHASE power

The UPFC system model downloaded from the reference link [59] had five errors. Therefore, these errors were corrected with the SIMULINK software before using the model and explaining it in more detail in section 1.

1.5.2 RECTIFY FIVE ERRORS AND IMPROVE THE ORIGINAL SIMULINK MODEL

The reference model had errors because the FROM and GOTO blocks were not specified for the VI measurement block. These were fixed by either specifying or removing them. The removal method was applied because these blocks were only used for creating the BUS structure, which was not a BUS block. Thus, the errors were eliminated. On the other hand, some theoretical errors were not caused by the system but by the assumption. This error stated that the overall power system required an Alternating current, which was not provided in this model after UPFC operation and the final outputs were Pulsating AC. Therefore, this error was also corrected along with the overall proposed system and presented in this section and the final figure is Fig. 3.

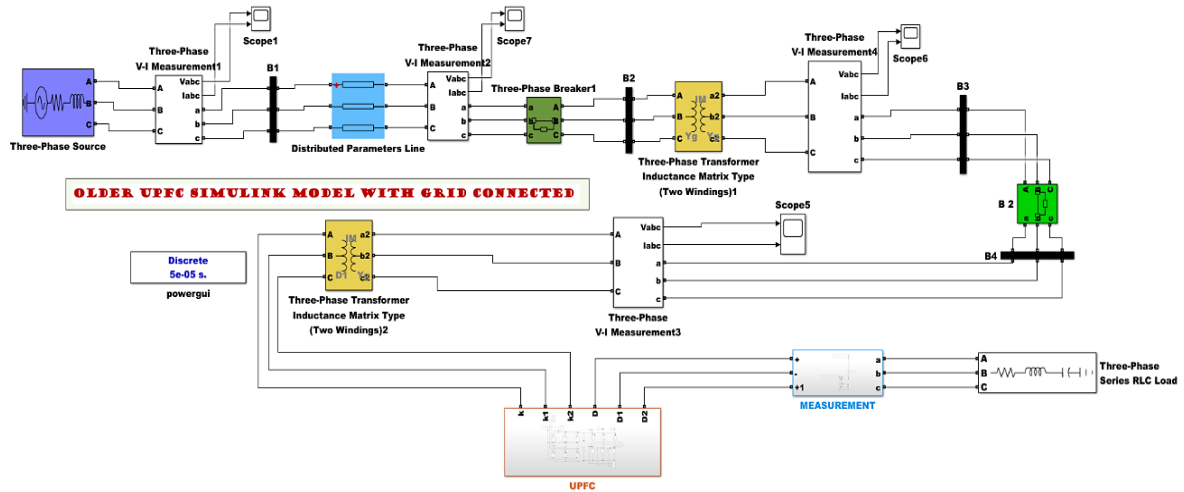


Figure 2. Error in the referred system

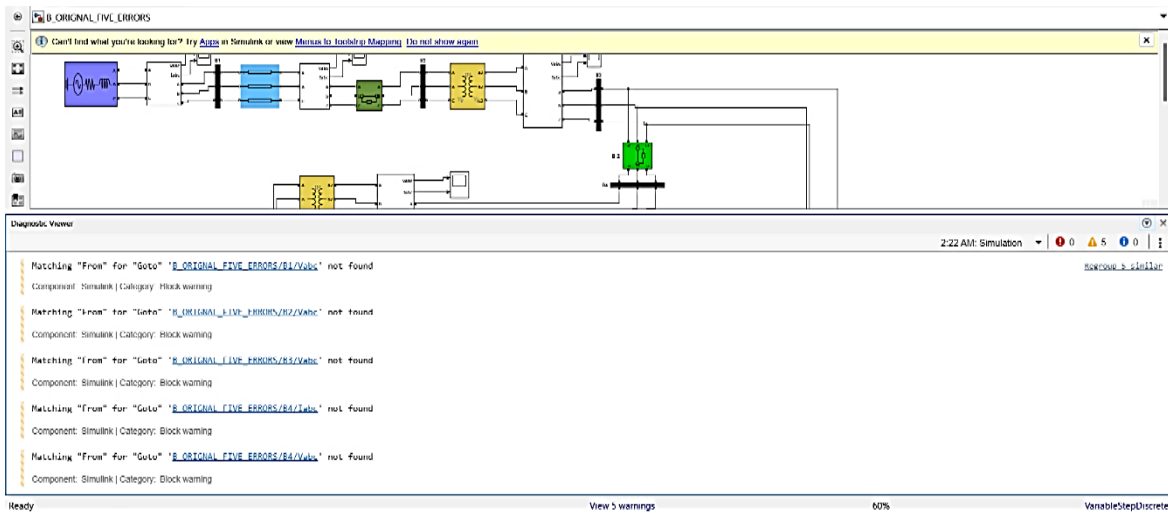


Figure 3. Older UPFC model

1.5.3 PV ARRAY SIMPLE OPERATION

The components of the proposed system need to be examined before implementing it. Therefore, the following system is built to test the PV system to verify that the single-line phase operates according to the ideal concept.

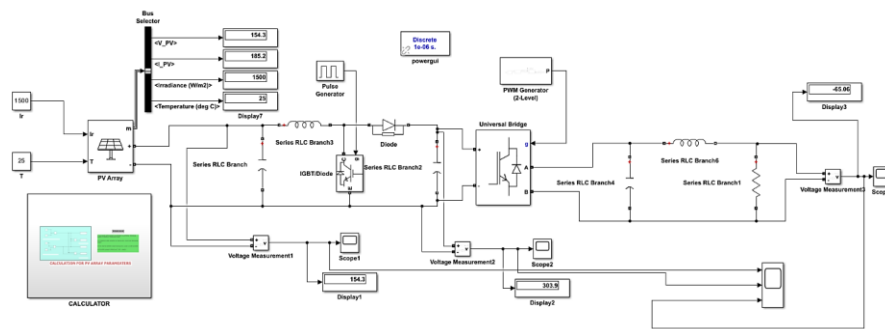


Figure 4. PV array and its operation up to dc to ac

The model produces correct outputs after running in a SIMULINK environment. The DC output displays a voltage of up to 154 volts and a current of nearly 185.2 ampere as shown in figure number 4. This system aims to generate pure AC power from the DC output of the PV array, so the voltage is boosted to 303.9 volts after the boost operation. Then, the same voltage is fed into the inverter and the output becomes pulsating AC. Moreover, this pulsating AC is passed through passive filters to reduce the harmonics and obtain pure AC power.

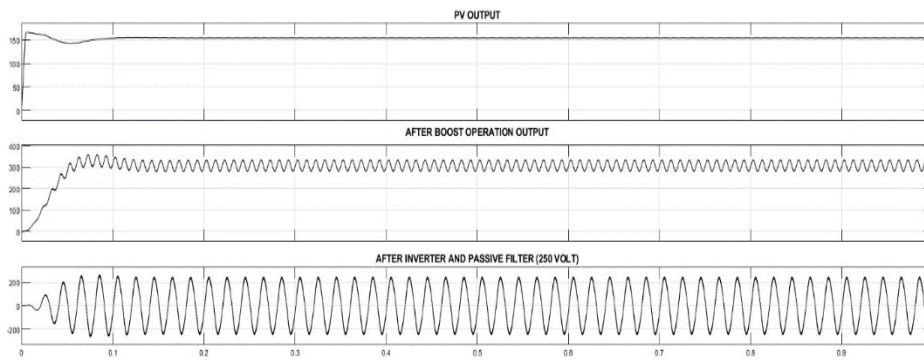


Figure 5. PV, Boost Converter, Passive Filter waveform

Figure number 5 shows the waveforms related to the PV, Boost Converter, and Passive Filter that have been discussed. The PV and boost converter output are DC waveforms and the passive filter output is a pure AC waveform.

1.5.4 Testing of the proposed overall system

After completing all of the models, the overall model design using combine these models in one place in SIMULINK. Because now the aim of testing is to the analysis of an improved system with a SOLAR POWER system. Figure 8 illustrates the overall proposed system.

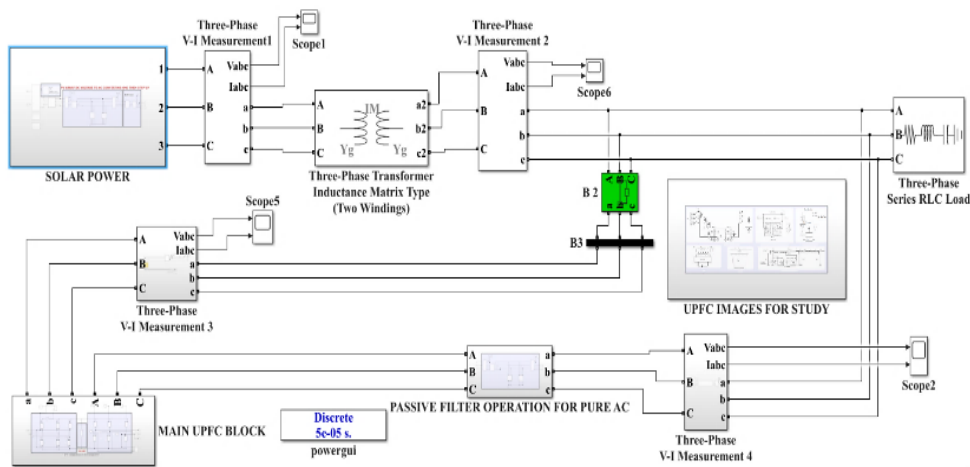


Figure 6. proposed the overall system

In Figure number 9 illustrate the PV system

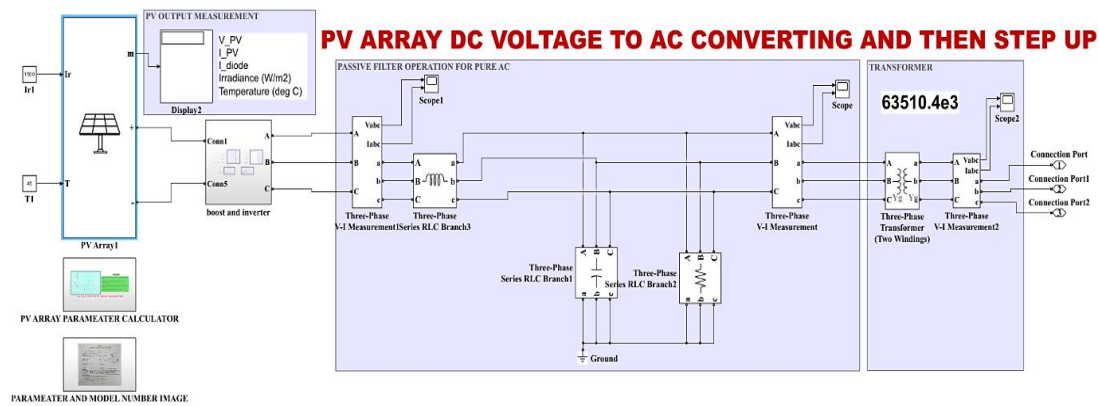


Figure 7. PV SYSTEM

Figure number 6.10 illustrate the design of the SIMULINK model of the main block for UPFC.

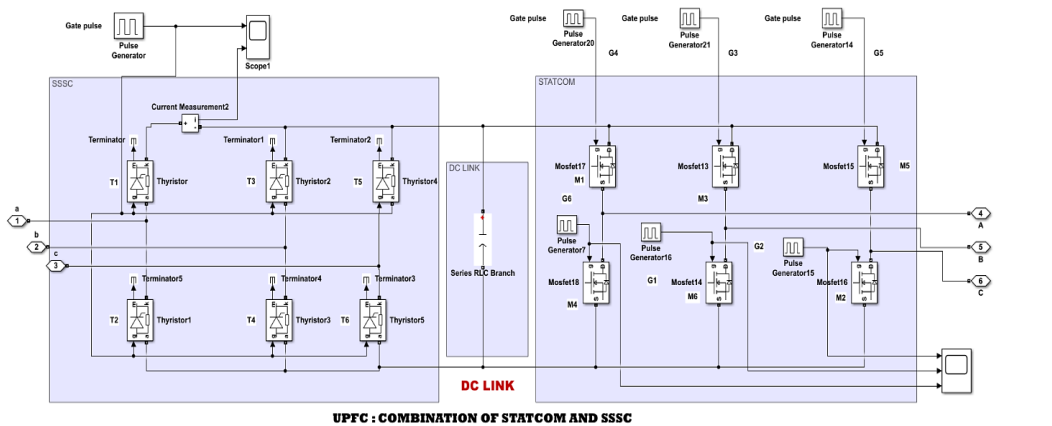


Figure 8. Model of STATCOM AND SSSC

Figure number 6.11 shows that the PV array data are used to generate power for the main system or also known as UPFC and load.

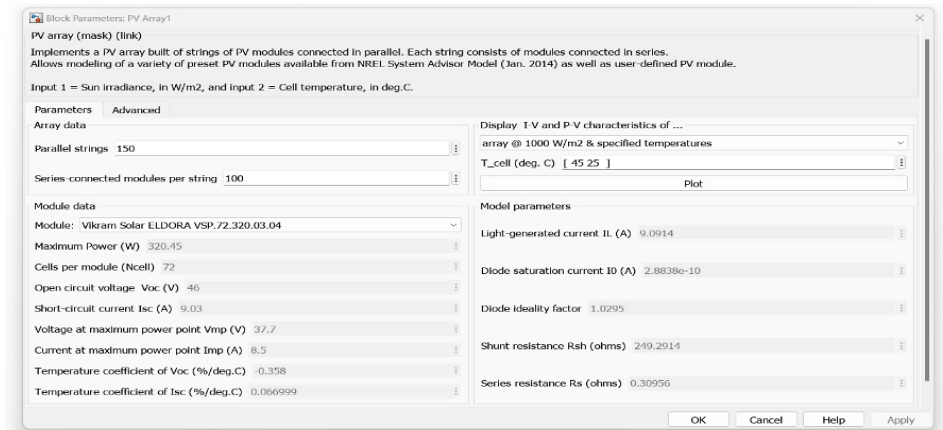


Figure 9. PV ARRAY PARAMETER

Figure number 12 is used for designing the exact data requirement of PV ARRAYS in the number of parallel and series for required power generation. Here ohms law is used for the calculation of voltage, current and resistance. Further, the power calculation was done using the $P=V \cdot I$ formula.

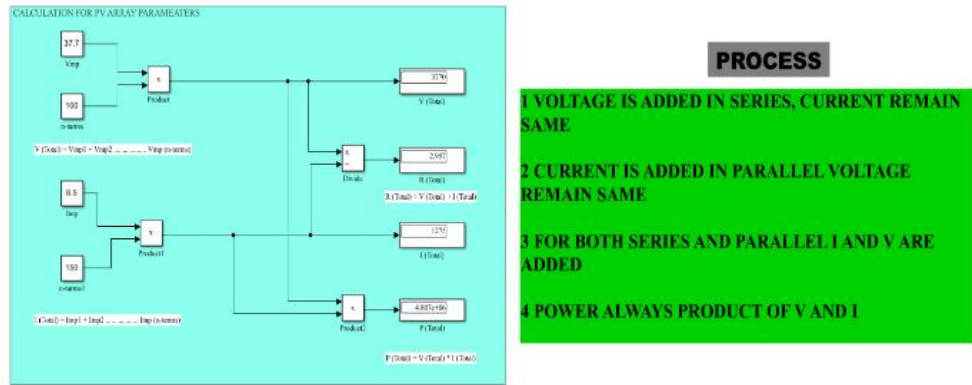


Figure 10. PV Array parameters calculation

Figure number 13 describes that the boost converter can up the power up to the needful range. PV array voltage given in the same figure shows 3581 volts and after boost operation same voltage increases by nearly 7186 volts.

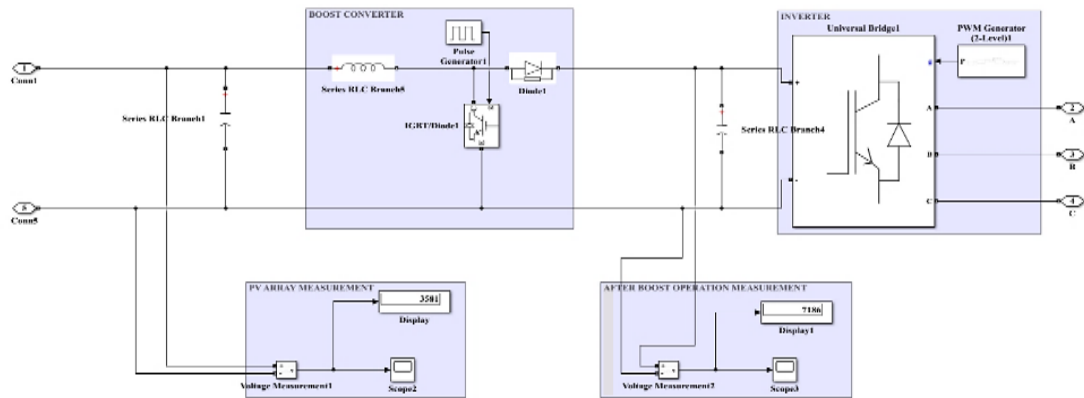


Figure 11. Operation of Boost Converter and Inverter

Figure number 15 describes the waveform for a step-up voltage after the operation of the step-up transformer.

	Value	Time
Max	6.748e+04	1.549
Min	-6.582e+04	1.559

Figure 12. Maximum and Minimum Values

In Figure number 14, the maximum and minimum value is given after the operation of the transformer in step-up mode. This is also demonstrated using the waveform and these outputs are displayed using a scope block. Figure 15 describes the same outputs in the sinusoidal waveform.

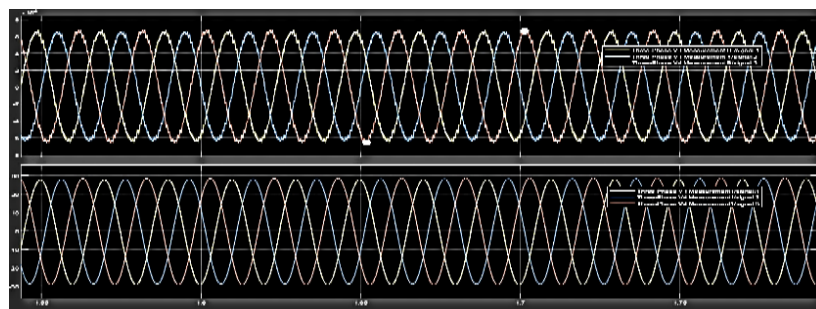


Figure 13. V And I value after Transformer Operation

In this system, UPFC works on 254.3 volts of pulsating AC. For this reason, the high voltage was down by another transformer from 63510.4 to 254.03 volts. Therefore, after UPFC operation we just need to install a load to get a stable value of power. Further, when the system is run on these all above-given operations then the output gets out as Figure number 16. Hence, figure 16 shows that the output voltage is nearly 250 volts.

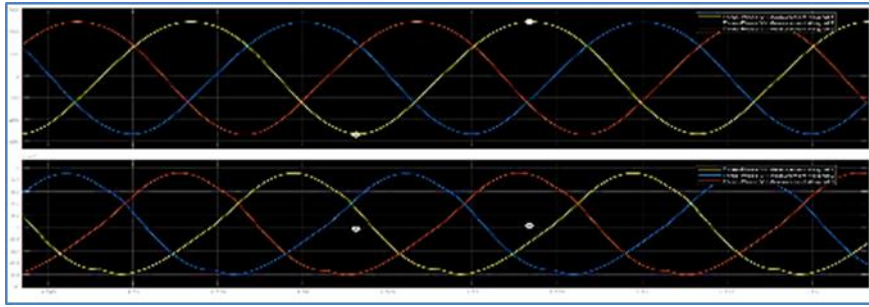


Figure 14. After UPFC and Passive Filter

1.6 Conclusion

The analysis and simulation results show that the proposed system can perform any kind of calculation based on the PV array and UPFC data. The main objective of this work was to test the whole system in the SIMULINK environment, which has been achieved successfully. This chapter ends here and the next chapter presents the results and conclusion based on the real-time data and parameters of GNDEC, Ludhiana in SIMULINK software.

1.7 Data Collection

1.4.1 TOTAL PV ARRAY OF ROOFTOP OF PRINCIPAL OFFICE

The Principal Office at Guru Nanak Dev Engineering College has a rooftop with several Array Modules, as shown in Table 1. These PV arrays are connected in a combination of series and parallel modules.

Table 2. Calculation of the total number of PV MODULE

Sr. No.	Array in numbers	Array in numbers	Array in numbers	Number of Lines	Multiply or add	Total of all
1.	8					8
2.	10					10
3.	16			6	*	96
4.	24			2	*	48
5.	26					26
6.	20					20
7.	8					8
8.	8					8
9.	12					12
10.	12	4	12		+	28
11.	12		12		+	24
12.	8	10	8		+	26
						=314

PARAMETER images of a single PV array Module are shown in Figure number 17 below.



Figure 15. Parameter figure for PV array Model

1.4.2 Data collection for one month

Table number 7.2 has been demonstrating the one-month data from 04-04-2023 to 03-05-2023 on energy generated, expected energy and radiation with performance ratio. In between these days, energy generated data look different from the range 68 to 404 (in kWh). The radiation or most say irradiance range occurs from 7 and gradually increases or decreases in these days to nearly 13 (in kWh/m²). The performance ratio table also performs visible during these different days which do not reach up to 50 per cent.

Time	Energy Generated(kWh)	Expected Energy(kWh)	Radiation(kWh/m ²)	Performance Ratio (%)
04-04-2023	391	1034.76	10.35	37.79
05-04-2023	313	1043.58	10.44	29.99
06-04-2023	349.2	1191.68	11.92	29.3
07-04-2023	402.32	1216.32	12.17	33.08
08-04-2023	401.02	868.92	8.69	46.15
09-04-2023	395.91	1198.45	11.99	33.04
10-04-2023	376.65	930.85	9.31	40.46
11-04-2023	349.81	1001.87	10.02	34.92
12-04-2023	357.17	1150.42	11.51	31.05
13-04-2023	363.38	887.21	8.88	40.96
14-04-2023	363.65	898.27	8.99	40.48
15-04-2023	361.47	1085.76	10.86	33.29
16-04-2023	340.99	799.81	8	42.63
17-04-2023	300.85	942.92	9.43	31.91
18-04-2023	288.41	1077.41	10.78	26.77
19-04-2023	374.57	987.24	9.88	37.94
20-04-2023	361.21	1090.58	10.91	33.12
21-04-2023	403.42	1112.9	11.13	36.25
22-04-2023	68.1	1254.52	12.55	5.43
23-04-2023	320.12	1077.34	10.78	29.71
24-04-2023	372.43	936.85	9.37	39.75
25-04-2023	395.22	809.68	8.1	48.81
26-04-2023	345.93	955.48	9.56	36.2
27-04-2023	216.56	1002.07	10.03	21.61
28-04-2023	355.62	1039.94	10.4	34.2
29-04-2023	320.76	844.02	8.44	38
30-04-2023	281.54	1059.4	10.6	26.58
01-05-2023	155.67	975.02	9.75	15.97
02-05-2023	172.87	1057.2	10.58	16.35
03-05-2023	349.44	969.09	9.7	36.06

Table 3. Data of Energy Generated(kWh), Expected Energy(kWh), Radiation(kWh/m²), Performance Ratio (%)

1.4.1. Data collection related to the PARAMETERS

Table 4. Parameter detail for SIMULINK Model

Sr. No.:	PV PARAMETERS:	Values
1.	Irradiance (W/m ²)	1000-1500
2.	Temperature (Deg. C)	25-45
Sr. No.:	ARRAY DATA:	Value
1.	Parallel strings	157
2.	Series-connected modules per string	157
Sr. No.:	MODULE DATA:	Value
1.	Module TYPE OR NAME	VIKRAM SOLAR ELDORA VSP .72.320.03.04
2.	The voltage at maximum power point Vmp (V)	37.7 V
3.	Current at maximum power point Imp (A)	8.5 A
Sr. No.:	I-V, P-V characteristics	Value
1.	Temperature of cell Tcell (deg. C)	25 degrees Celsius

In table number 7.3, the main data are represented for the PV array block of MATLAB/SIMULINK software.

2. Results

2.1 Introduction:

A PV array generates solar power and converts it from DC to AC, as discussed in section 3. A three-phase transformer steps down the voltage for UPFC operation. UPFC means UNIFIED POWER FLOW CONTROLLER and it enhances the power flow capability of the main system. Passive filters convert the pulsating AC into a pure sinusoidal AC waveform. An electrical load consists of motors, light bulbs, fans and other similar devices. They work for a small area and supply power to the same type of device, so the three-phase series RLC is set to standards.

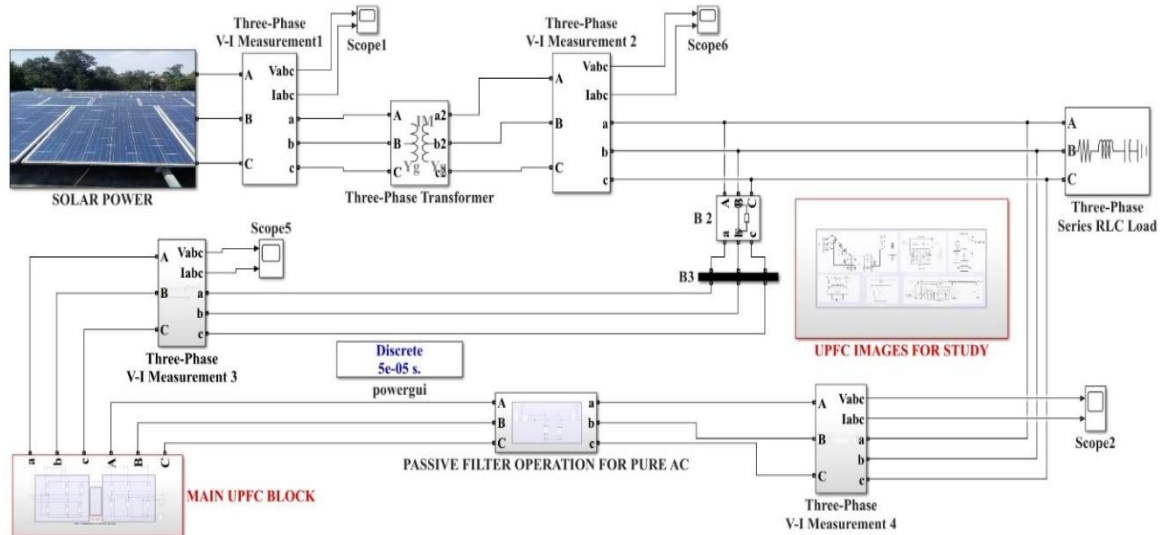


Figure 18. Overall system

2.2 PV System or SOLAR POWER

This is a subsystem that contains more subsystems and their circuits for generating SOLAR POWER. A SOLAR POWER system or a PV system has different parts that convert the DC power of a PV array into a sinusoidal waveform. These parts are as follows. PV array block has a parameter window to enter the data related to PV MODULE or PV model. This is a subsystem that has voltage, current and power measurement scopes and displays. Boost converter operation was explained in the introduction part and its circuit diagram was also shown there. Passive filter operation and this subsystem also have a passive filter circuit that converts the pulsating AC into pure AC. The transformer can change the voltage to a specific range. Here, a step-up transformer is used to increase the power to the required level for the whole system.

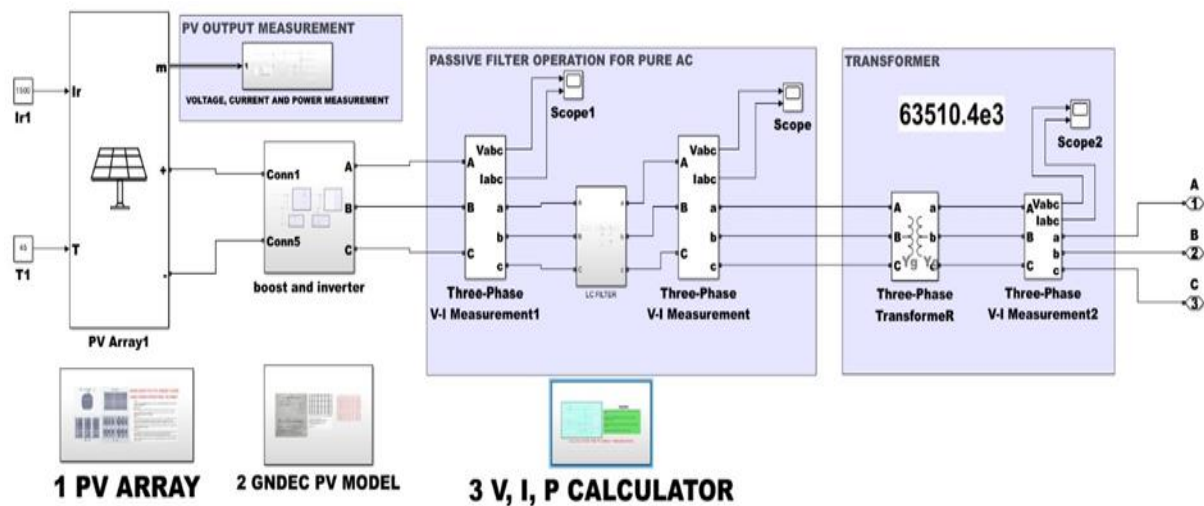


Figure 19. SOLAR POWER

2.3 PARAMETER USED

Data need for the generation of solar energy for all parameters are shown in Figure 8.3.

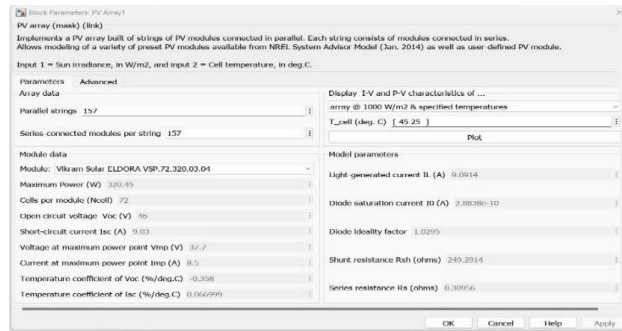


Figure 20. PV array data

2.4 RESULTS AND OUTCOMES

2.4.1 PV OUTPUTS

In Figure number 8.4, the value ranges out to 4461 volts for the PV array. Current 2129, Power 9.497 and resistance 2.095.

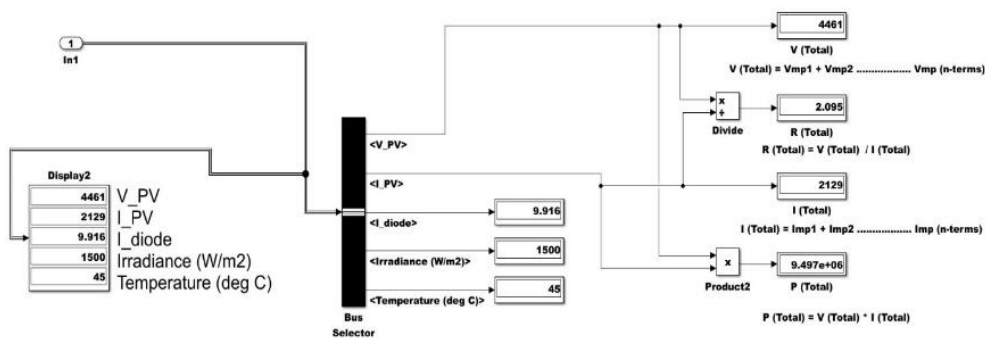


Figure 21. V, I, and P MEASUREMENT

2.4.2 OUTPUTS AFTER AND BEFORE BOOST OPERATION

In waveform figure number 8.5, Before the value range out to 4461 volts for the PV array and after the boost-up range increases up to 9000.

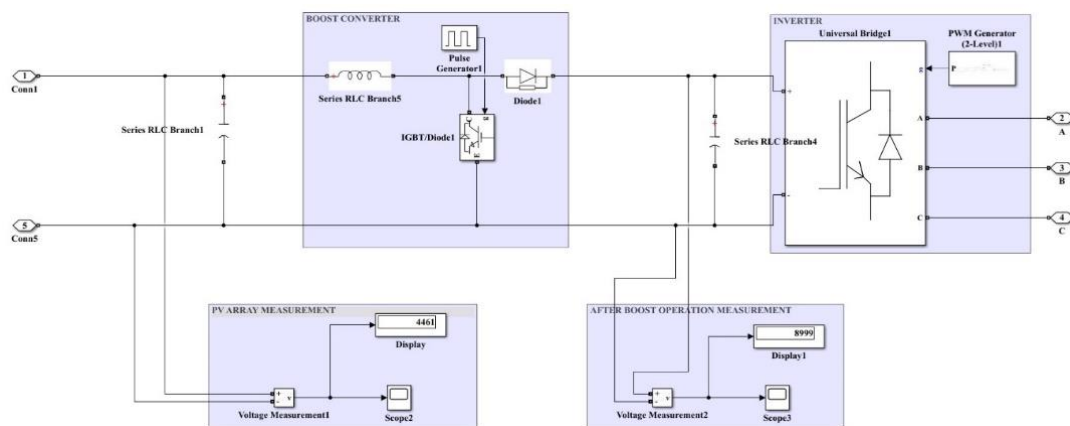


Figure 22. Boost and inverter operation

2.4.3 OUTPUT FROM INVERTER

In waveform figure number 8.6, the Value range out 6000 volts.

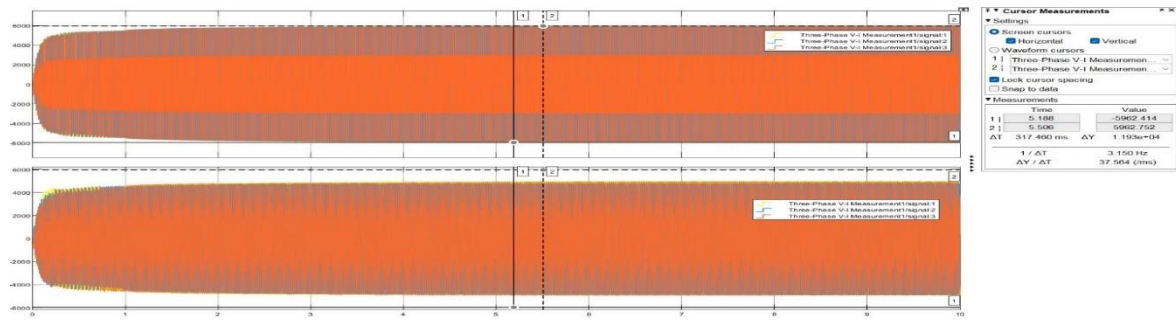


Figure 23. PULSATING AC FROM INVERTER

2.4.4 PASSIVE FILTER OPERATION

In waveform figure number 8.7, the Value range out to 1400 volts.

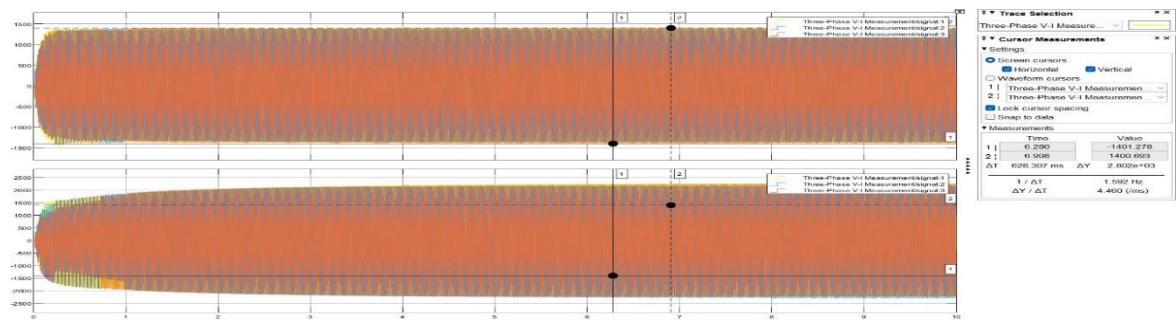


Figure 24. AFTER PASSIVE FILTER

2.4.5 TRANSFORMER OPERATION

In waveform figure number 8.8, the Value range out to 80750 volts.

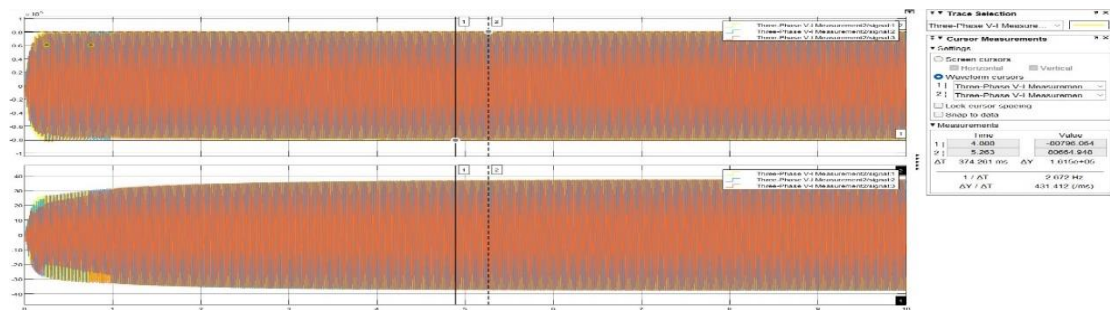


Figure 25. AFTER STEP-UP

2.4.6 STEP DOWN OPERATION

In waveform figure number 8.9, the Value range out 330 volts.

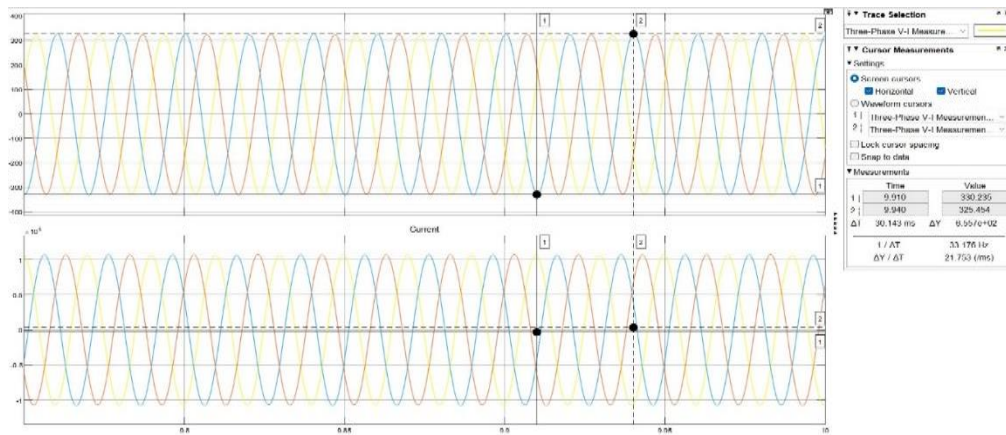


Figure 26. AFTER STEPPING DOWN FROM UPFC

2.4.7 UPFC AND PASSIVE FILTER OPERATION

In waveform figure number 8.10, the Value range out 330 volts.

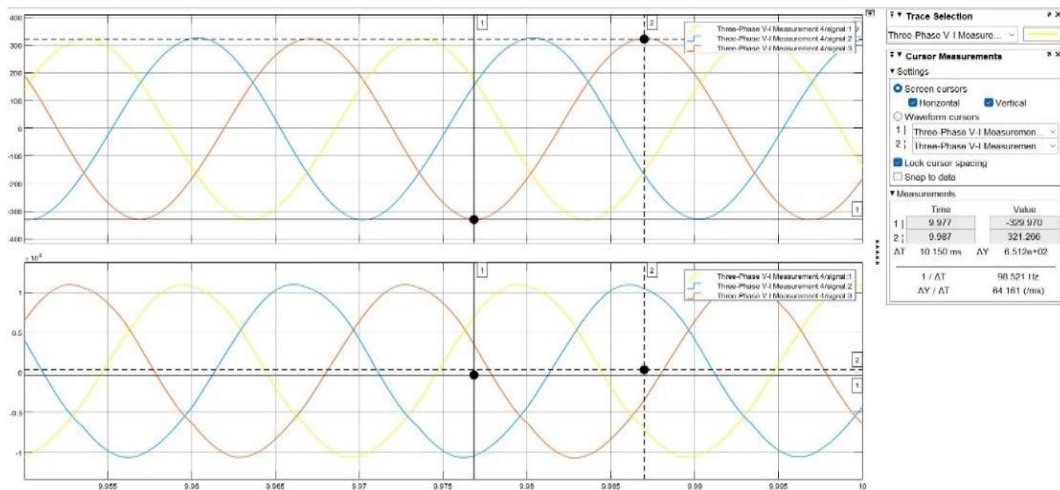


Figure 27. AFTER UPFC AND PASSIVE OPERATION

3. Conclusion

The paper has investigated the application of a UNIFIED POWER FLOW CONTROLLER (UPFC) to enhance the power system performance of a model that includes a PV system. The UPFC is a FACTS device that comprises a Static Synchronous Series Compensator (SSSC) and a Static Synchronous Compensator (STATCOM) with a shared DC link. The research paper has also presented the PV system and its components, and the methods of converting PV DC to pure sinusoidal AC waveform. The model was built and simulated in MATLAB (SIMULINK) using data and parameters from real-time calculations. The results demonstrated that the UPFC improved the power quality and stability of the model under various operating conditions. The paper has also proposed some potential applications and future directions for the model, such as combining it with nuclear or solar power resources or using it for desalination purposes.

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